



**9th Edition of Summer Training
Program on VLSI Design (STPVD)**
24th June to 21st July, 2024 (One Month)

Technically Co-Sponsored by
IEEE
UP SECTION (INDIA)
CAS CHAPTER

OVERVIEW

To cater the design aspects of VLSI, and to contribute in the mission to make industry ready engineers, the department of Electronics & Communication Engineering is organizing a **one month Summer Training Program on VLSI Design (STPVD)- 2024** from *24th June to 21st July 2024*.

Last date of registration:

25 April, 2024

Limited seats:

On first come first serve basis

REGISTRATION DETAILS

Registration fee | Rs 8000+18%GST= 9440

Extra charges (if availed)

Hostel charges | Rs 118 per day

Food charges | Approx Rs 160 per day

Register by clicking stpvd.iita.ac.in

Name of the Bank: **SBI**

Account Name: **Indian Institute of Information Technology**

Account Number: **30996838478**

IFSC Code: **SBIN0010891**

Opportunity to do internship is also available

COURSE HIGHLIGHT

Opportunity to learn

IC Design flow
Logical Efforts
Memory Design
Static timing analysis
Arithmetic Blocks
Scaling Effects
Design of Amplifiers
Transceiver Design
Mixed Signal Design

Opportunity to do

Projects on Data Converters
Projects on Transceiver Design
Projects on Processor Design
Projects on Bandgap Reference
Projects on Memory Design

Other Opportunities

Interaction with Industry Experts
Visit to clean room
Learning of HDL & Hands on EDA
Reward for BEST Project

Coordinators- Dr Kavindra Kandpal, Dr Prasanna Misra, Prof Manish Goswami

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